

Notice of References Cited	Application/Control No. 09/928,848		Applicant(s)/Patent Under Reexamination MANDELL ET AL.	
	Examiner Akash Saxena		Art Unit 2128	Page 1 of 2

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,933,356	08-1999	Rostoker et al.	703/15
*	B	US-6,757,884	06-2004	Mandell et al.	716/18
	C	US-6,182,258	01-2001	Hollander, Yoav	714/739
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	https://solvnet.synopsys.com ; Synopsys Solvit Doc Id: 901665, Product: Behavioral Compiler, Topic: "use_netlist attribute with preserve function" Last Updated: 10/23/1998
	V	https://solvnet.synopsys.com ; Synopsys Solvit Doc Id: 901157, Product: Behavioral Compiler, Topic: "Using Preserved Functions in Behavioral Compiler" Last Updated: 10/16/2001
	W	https://solvnet.synopsys.com ; Synopsys Solvit Doc Id: 002613, Product: (V)HDL Compiler, Topic: "Verilog and VHDL Directives" Last Updated: 10/8/2002
	X	https://solvnet.synopsys.com ; Synopsys Solvit Doc Id: 901336, Product: Design Compiler, Topic: "The partition_dp command does not work on a Mapped Design" Last Updated: 8/2/2001

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Notice of References Cited	Application/Control No. 09/928,848	Applicant(s)/Patent Under Reexamination MANDELL ET AL.	
	Examiner Akash Saxena	Art Unit 2128	Page 2 of 2

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	https://solvnetsynopsys.com ; Synopsys Solvit Doc Id: 901884, Product: Design Compiler, Topic: "Case Selector Bit Ranges Not Valid" Last Updated: 11/14/2000
	V	https://solvnetsynopsys.com ; Synopsys Solvit Doc Id: 903912, Product: Design Compiler, Topic: "Behavioral Compiler Information Roadmap" Last Updated: 10/23/2001
	W	"Computer-Aided Partitioning of Behavioral Hardware", McFarland, M.C., S.J. This paper appears in: IEEE, Design Automation Conference Proceedings 1983, 20th, Pages:472-478□□
	X	"Assignment Decision Diagram and its Uses in High-level Synthesis", V. Chaiyakul and D.D. Gajski, Technical Report #9--103, University of California Irvine, October 1992.

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.